

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Rory Dickman

Serial No.: 10/672,145

Filed: September 26, 2003

For: METHOD FOR  
CONTROLLING  
SEMICONDUCTOR CHIPS  
AND CONTROL  
APPARATUS

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Patent No.: 6,986,118

Issued: Jan. 10, 2006

Group Art Unit: 2825

Examiner: Sun J. Lin

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office to fax number 571-273-8300, or electronically transmitted via EFS-web, on the date shown below:

April 24, 2007  
Date

/Joseph M. Jong/  
Joseph M. Jong

REQUEST FOR CERTIFICATE OF CORRECTION

Attached is a Certificate of Correction for correcting several errors in the Detailed Description of the Preferred Embodiment section and Claims section of the printed patent.

Applicant submits that the errors mentioned above were not by the applicant, but were made during the printing of the patent.

Respectfully submitted, and  
**S-signed pursuant to 37 CFR 1.4,**

/Joseph M. Jong, Reg. No. 42,698/

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**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,986,118

Page 1 of 1

APPLICATION NO.: 10/672,145

ISSUE DATE: Jan. 10, 2006

INVENTOR(S): Rory Dickman

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 4, Line 49, replace "SDRAMS" with --SDRAMS--.

In Column 5, Line 3, replace "and also the high clock rates used can easily reach a" with -- and also the high clock rates used for operating them, semiconductor chips IC1-IC36 today can easily reach a--.

In Column 5, Line 47, replace "DJMMs" with -- DIMMs --.

In Column 5, Line 64, replace "memorychips" with -- memory chips --.

In Column 6, Line 59, replace "line" with --lines--.

In Claim 4, at Column 13, Line 13, replace "started." with --started up.--.

In Claim 9, at Column 13, Line 52, replace "date" with -- data --.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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PATTERSON & SHERIDAN, L.L.P.  
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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.